

FIG. 1

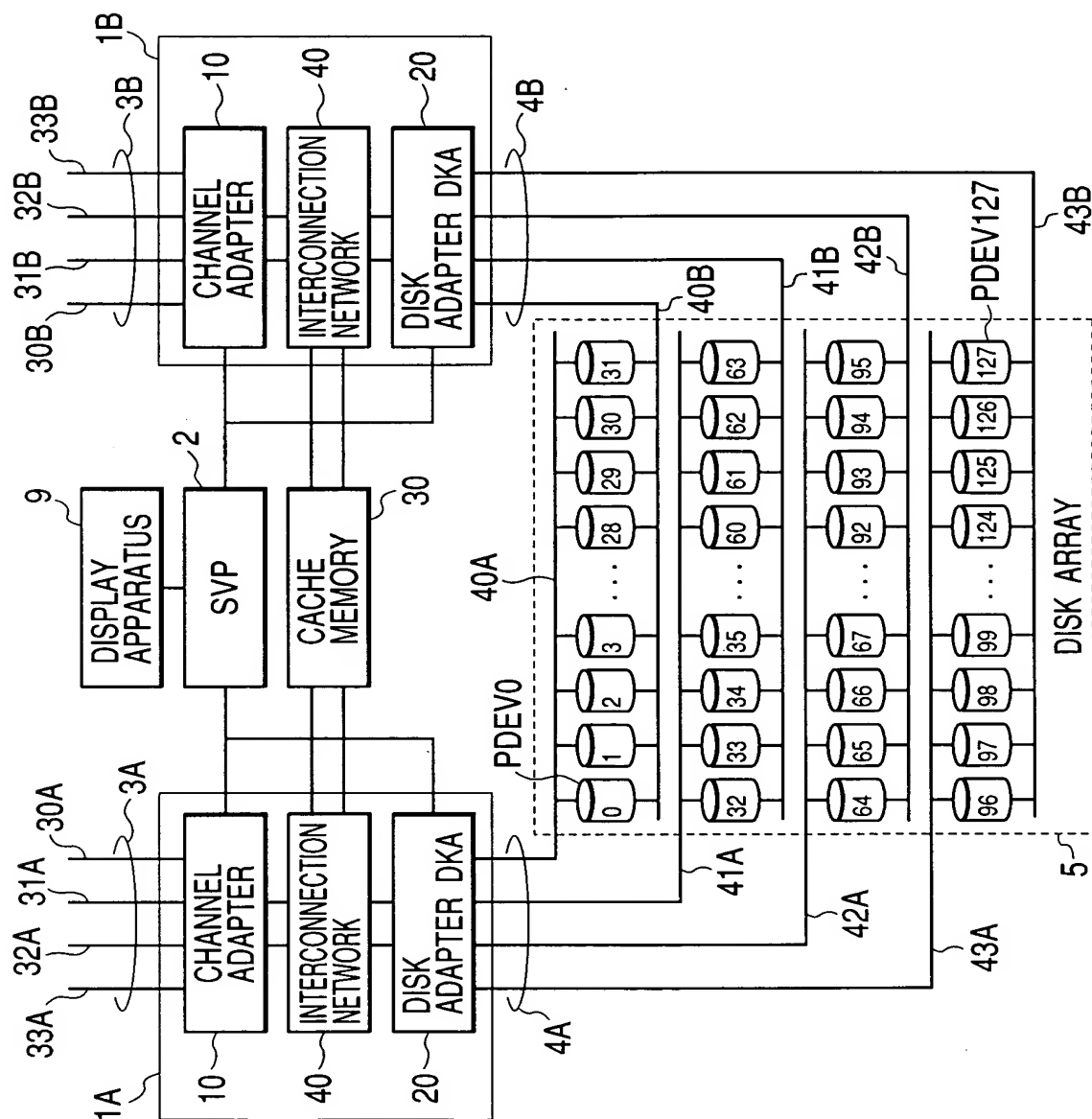


FIG. 2

CHANNEL ADAPTER 10

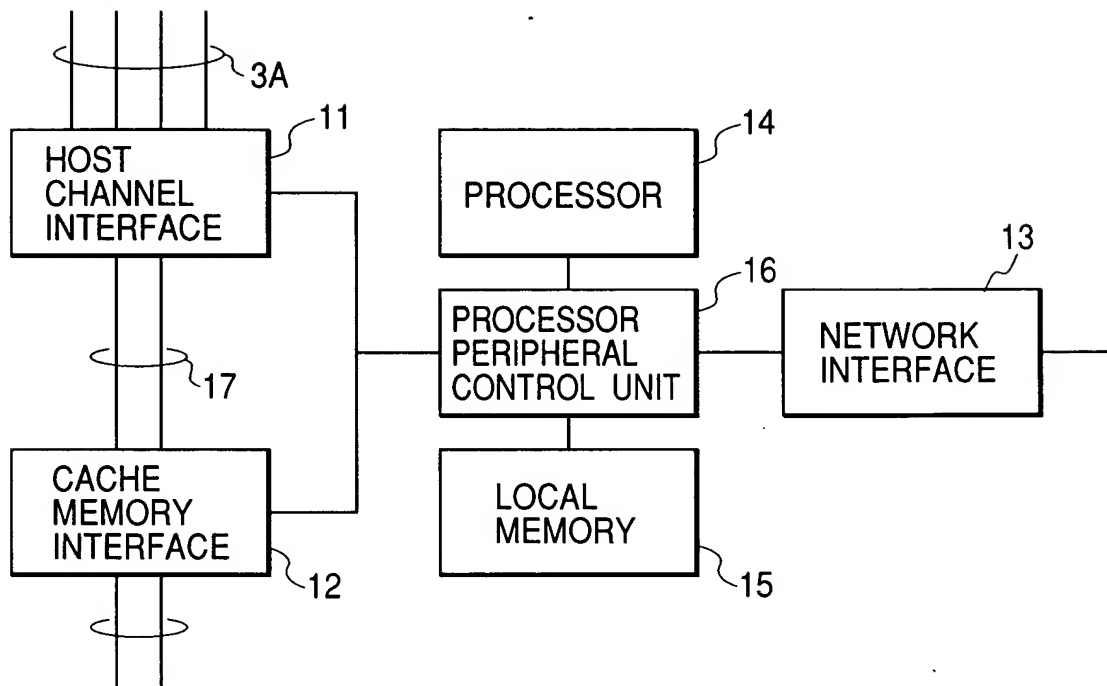
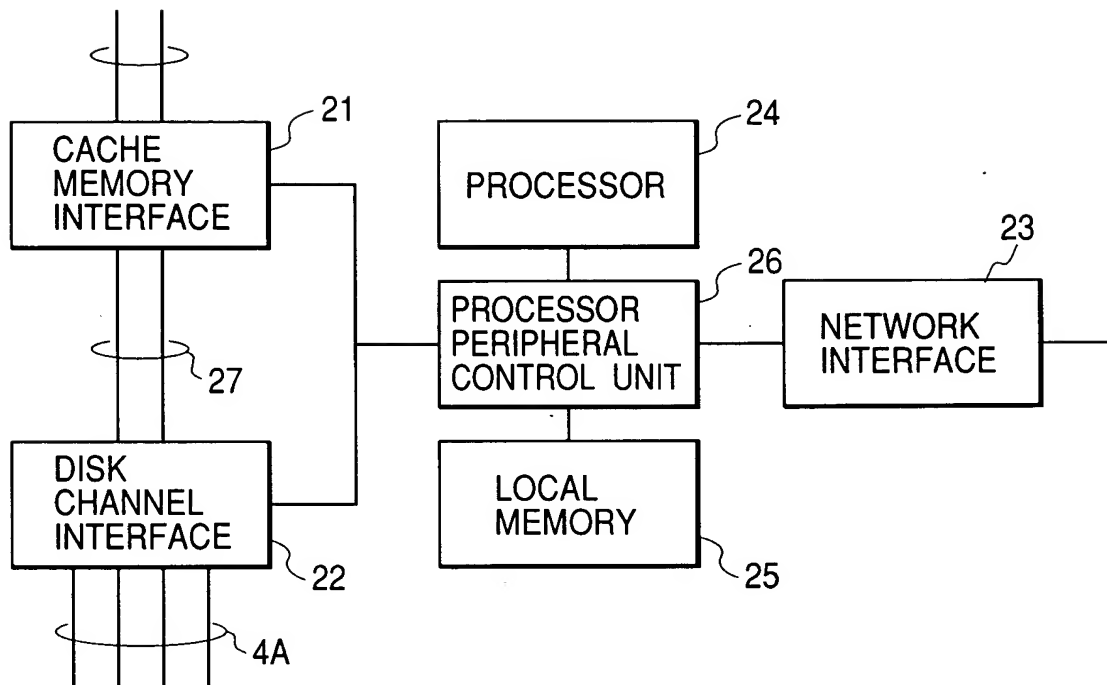


FIG. 3

DISK ADAPTER 20



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FIG. 4

DISK ARRAY 5

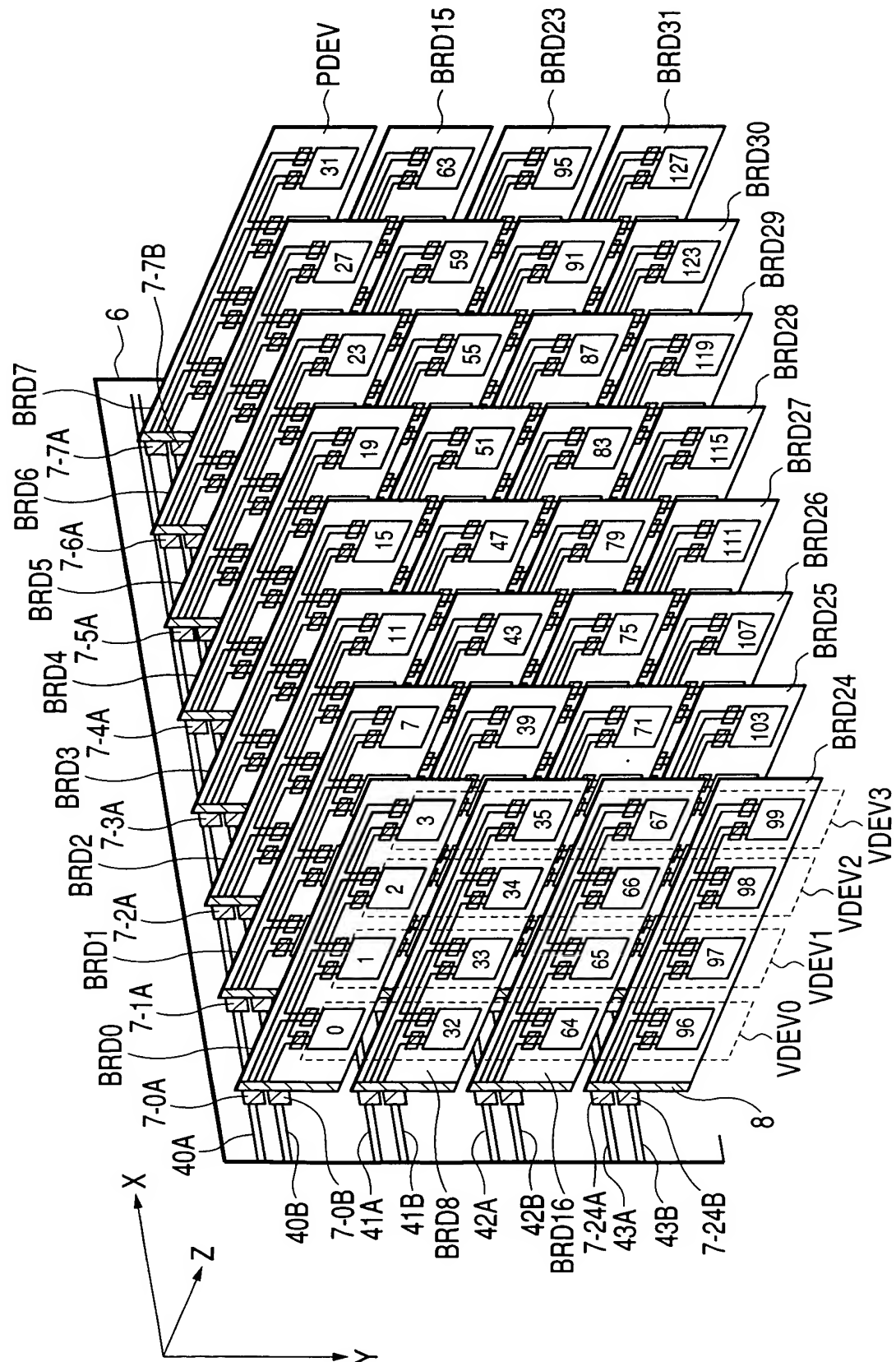


FIG. 5

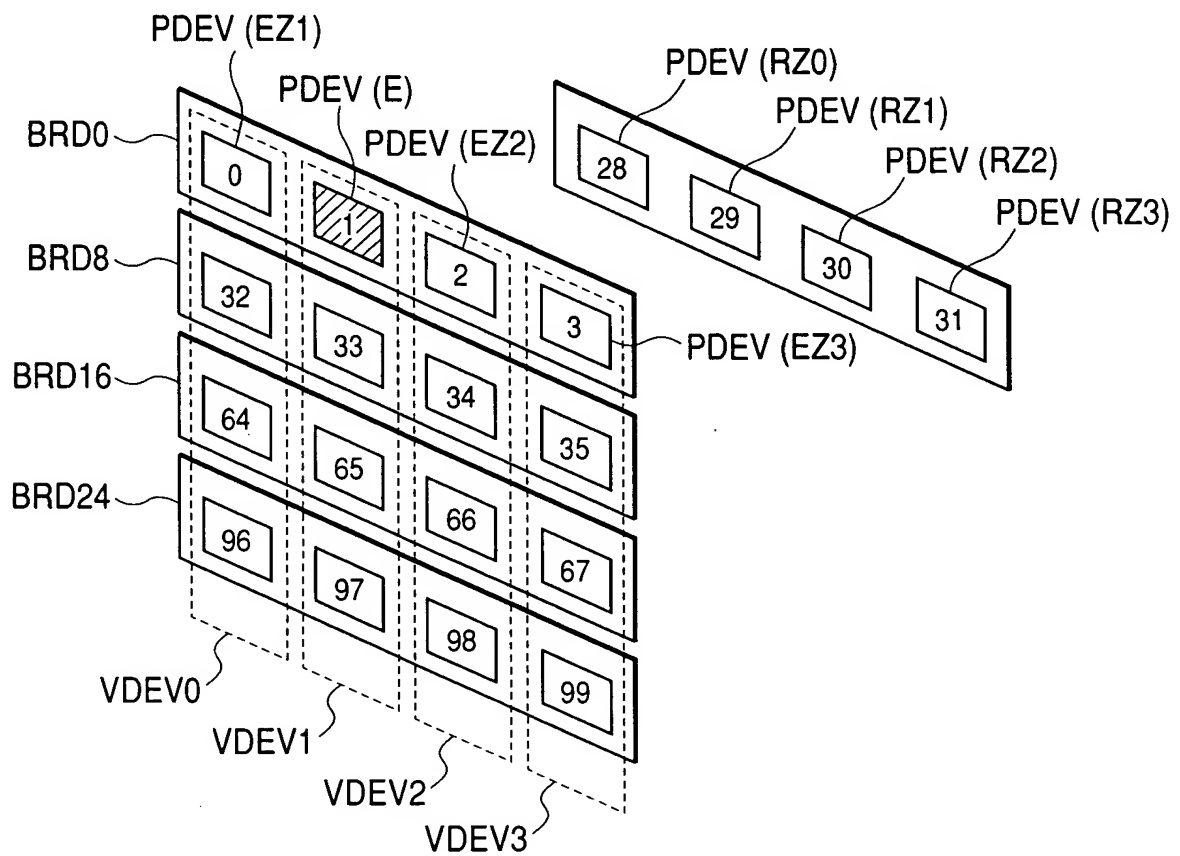


FIG. 6

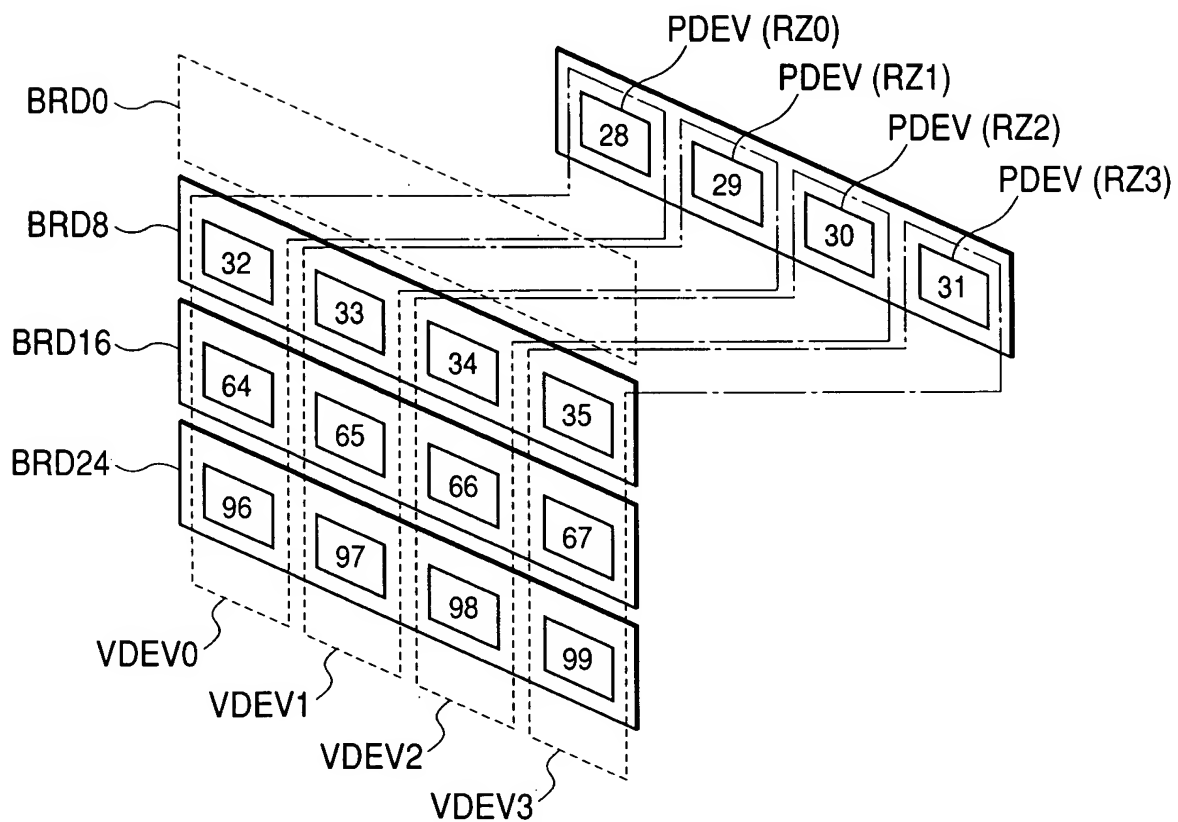


FIG. 7

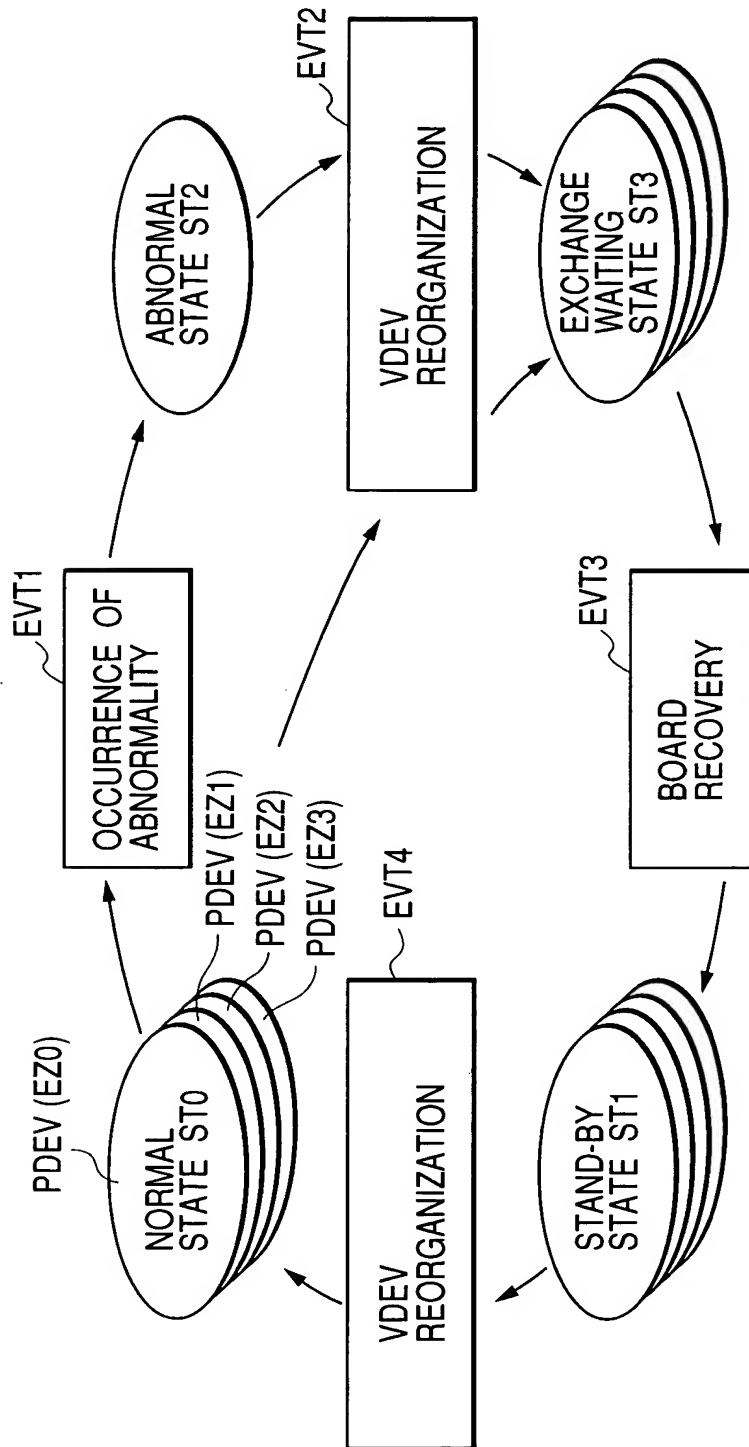


FIG. 8

PORT BYPASS CIRCUIT (PBC) 7

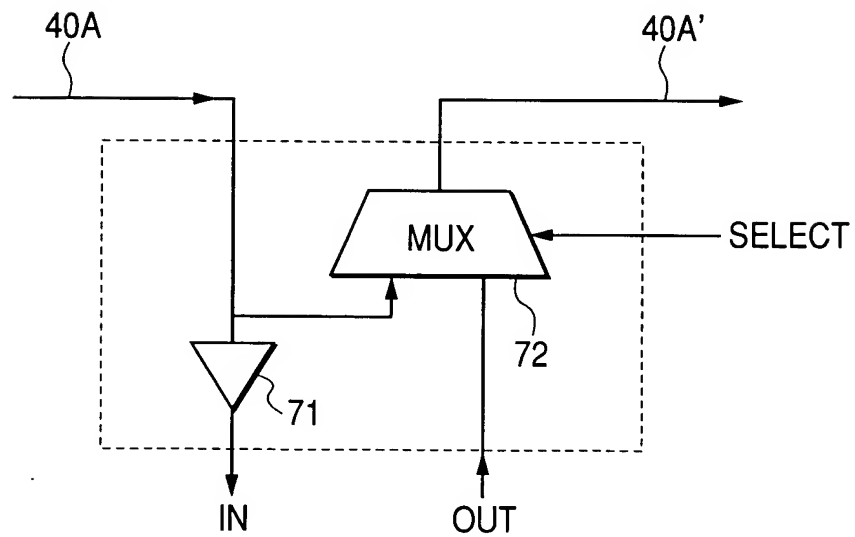
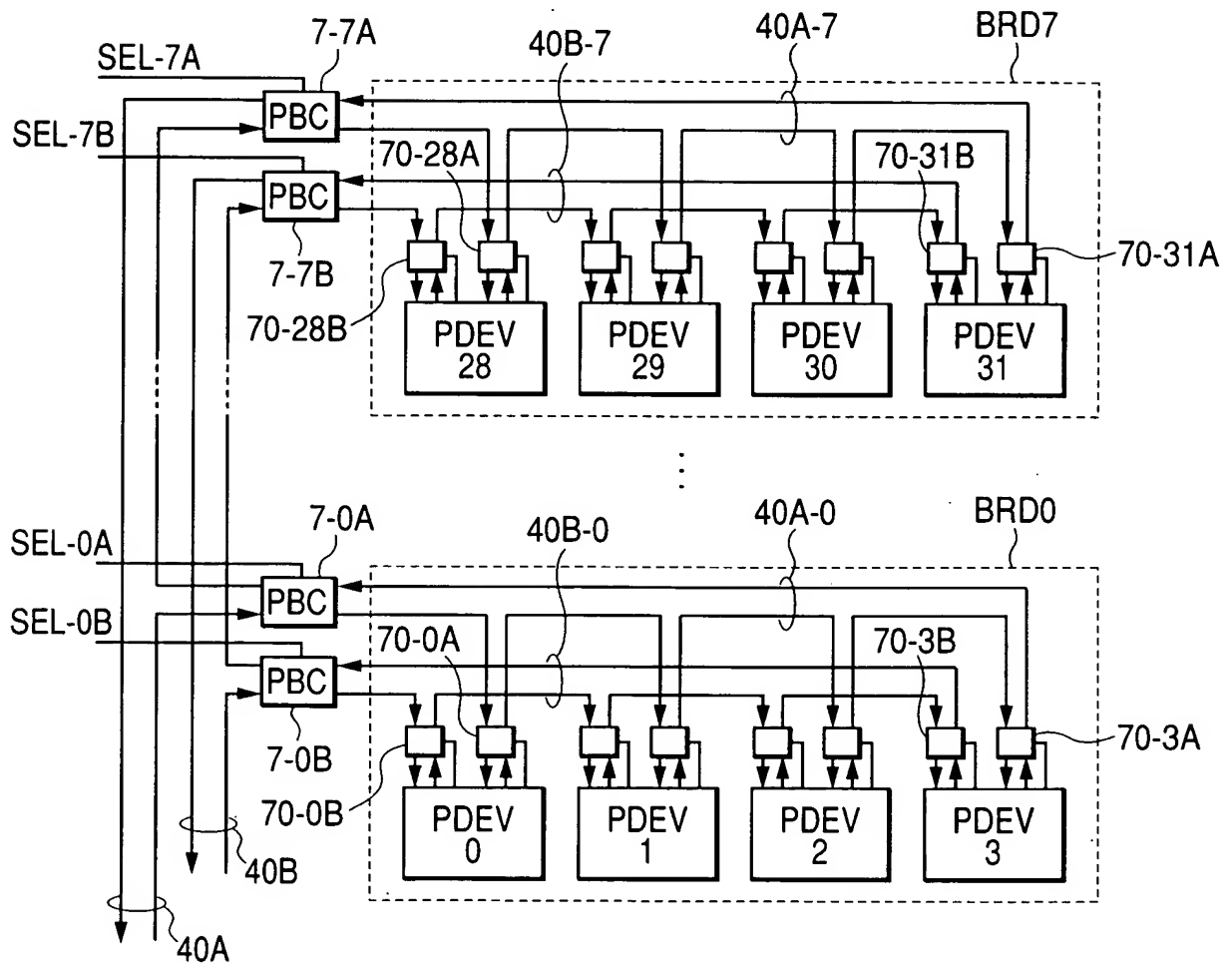


FIG. 9



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FIG. 10

BOARD MANAGEMENT TABLE 80

BOARD NUMBER: BRD	X COORDINATE	Y COORDINATE	BOARD STATUS CODE	DRIVE NUMBER: PDEV	
0	0	0	0	{0, 1, 2, 3}	800-0
1	1	0	0	{4, 5, 6, 7}	800-1
2	2	0	0	{8, 9, 10, 11}	800-2
3	3	0	0	{12, 13, 14, 15}	
4	4	0	0	{16, 17, 18, 19}	
5	5	0	0	{20, 21, 22, 23}	
6	6	0	1	{24, 25, 26, 27}	
7	7	0	1	{28, 29, 30, 31}	
8	0	1	0	{32, 33, 34, 35}	
9	1	1	0	{36, 37, 38, 39}	
10	2	1	0	{40, 41, 42, 43}	
11	3	1	0	{44, 45, 46, 47}	
12	4	1	0	{48, 49, 50, 51}	
13	5	1	0	{52, 53, 54, 55}	
14	6	1	1	{56, 57, 58, 59}	
15	7	1	1	{60, 61, 62, 63}	
16	0	2	0	{64, 65, 66, 67}	
17	1	2	0	{68, 69, 70, 71}	
18	2	2	0	{72, 73, 74, 75}	
19	3	2	0	{76, 77, 78, 79}	
20	4	2	0	{80, 81, 82, 83}	
21	5	2	0	{84, 85, 86, 87}	
22	6	2	1	{88, 89, 90, 91}	
23	7	2	1	{92, 93, 94, 95}	
24	0	3	0	{96, 97, 98, 99}	
25	1	3	0	{100, 101, 102, 103}	
26	2	3	0	{104, 105, 106, 107}	
27	3	3	0	{108, 109, 110, 111}	
28	4	3	0	{112, 113, 114, 115}	
29	5	3	0	{116, 117, 118, 119}	
30	6	3	1	{120, 121, 122, 123}	
31	7	3	1	{124, 125, 126, 127}	800-31

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FIG. 11
DRIVE MANAGEMENT TABLE 81

DRIVE IDENTIFICATION NUMBER (PDEV)	BOARD NUMBER	Z COORDINATE	MAXIMUM LOGICAL BLOCK ADDRESS	PDEV STATUS CODE
0	0	0	0x1FFFFFFF	0
1	0	1	0x1FFFFFFF	0
2	0	2	0x1FFFFFFF	0
3	0	3	0x1FFFFFFF	0
4	1	0	0x1FFFFFFF	0
5	1	1	0x1FFFFFFF	0
6	1	2	0x1FFFFFFF	0
7	1	3	0x1FFFFFFF	0
8	2	0	0x1FFFFFFF	0
9	2	1	0x1FFFFFFF	0
10	2	2	0x1FFFFFFF	0
11	2	3	0x1FFFFFFF	0
12	3	0	0x1FFFFFFF	0
13	3	1	0x1FFFFFFF	0
14	3	2	0x1FFFFFFF	0
15	3	3	0x1FFFFFFF	0
16	4	0	0x1FFFFFFF	0
⋮	⋮	⋮	⋮	⋮
32	8	0	0x1FFFFFFF	0
33	8	1	0x1FFFFFFF	0
34	8	2	0x1FFFFFFF	0
35	8	3	0x1FFFFFFF	0
36	9	0	0x1FFFFFFF	0
⋮	⋮	⋮	⋮	⋮
122	30	2	0x1FFFFFFF	1
123	30	3	0x1FFFFFFF	1
124	31	0	0x1FFFFFFF	1
125	31	1	0x1FFFFFFF	1
126	31	2	0x1FFFFFFF	1
127	31	3	0x1FFFFFFF	1

FIG. 12

RAID GROUP MANAGEMENT TABLE 82

GROUP IDENTIFIER (VDEV)	MASTER DISK ADAPTER NUMBER	RAID LEVEL	COMPONENT PDEV	NEW COMPONENT PDEV
0	0	5	{0, 32, 64, 96}	{28, 32, 64, 96}
1	0	5	{1, 33, 65, 97}	{29, 33, 65, 97}
2	0	5	{2, 34, 66, 98}	{30, 34, 66, 98}
3	0	5	{3, 35, 67, 99}	{31, 35, 67, 99}
4	1	5	{4, 36, 68, 100}	
5	1	5	{5, 37, 69, 101}	
6	1	5	{6, 38, 70, 102}	
7	1	5	{7, 39, 71, 103}	
8	0	5	{8, 40, 72, 104}	
⋮	⋮	⋮	⋮	
14	0	1	{14, 46}	
15	0	1	{15, 47}	
⋮	⋮	⋮	⋮	

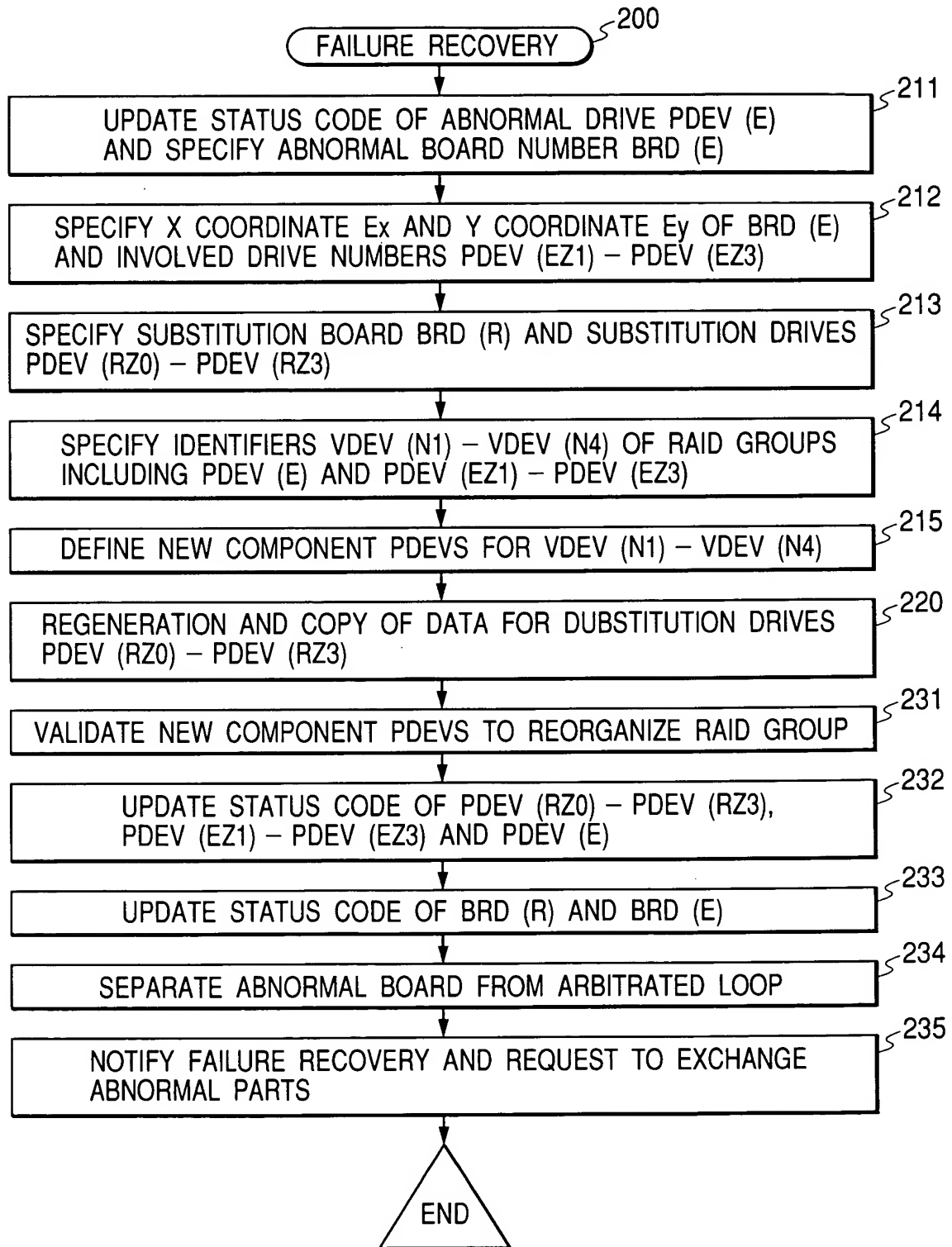
FIG. 13

FIG. 14

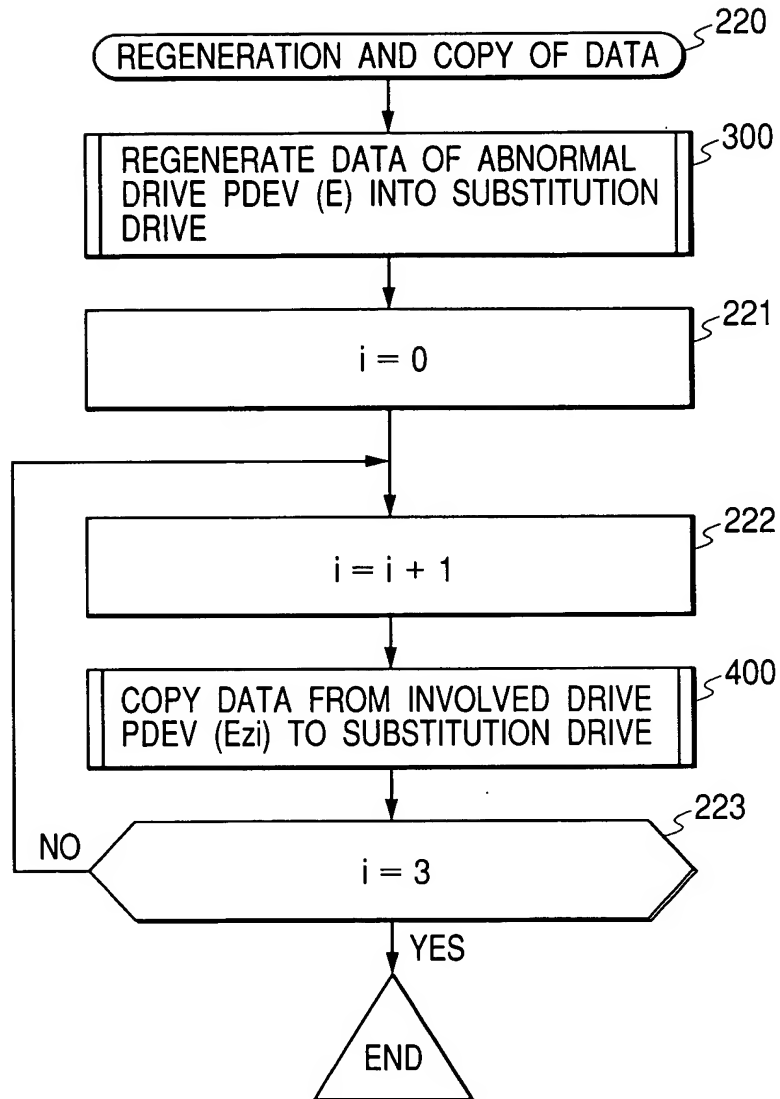


FIG. 15

DATA REGENERATION MANAGEMENT TABLE 83

AREA NUMBER	LOGICAL BLOCK ADDRESS		REGENERATION FLAG
	FROM	TO	
0	0x00000000	0x0000FFFF	1
1	0x00010000	0x0001FFFF	1
2	0x00020000	0x0002FFFF	1
3	0x00030000	0x0003FFFF	1
4	0x00040000	0x0004FFFF	0
5	0x00050000	0x0005FFFF	0
6	0x00060000	0x0006FFFF	0
7	0x00070000	0x0007FFFF	0
8	0x00080000	0x0008FFFF	0
9	0x00090000	0x0009FFFF	1
10	0x000A0000	0x000AFFFF	0

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FIG. 16

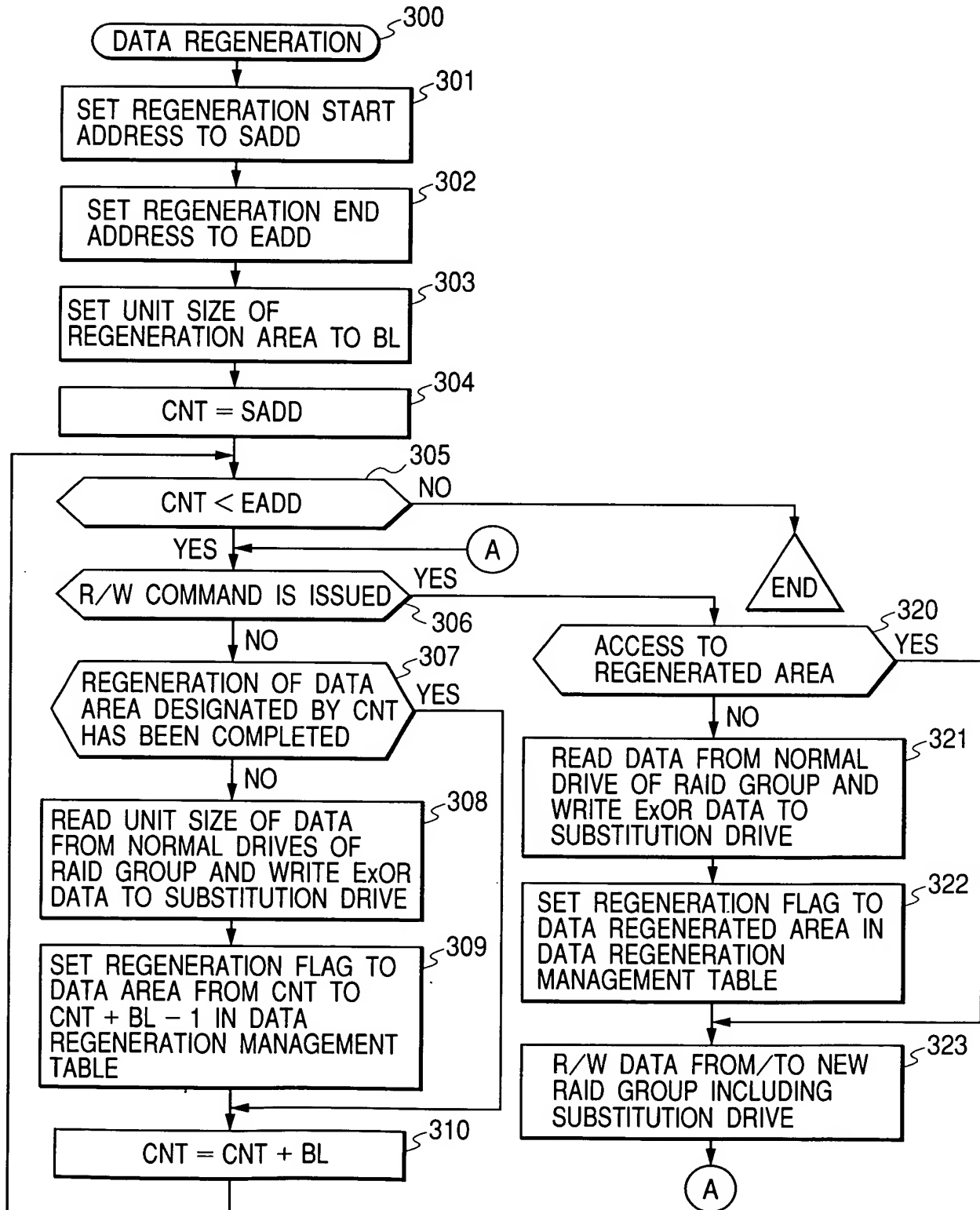


FIG. 17

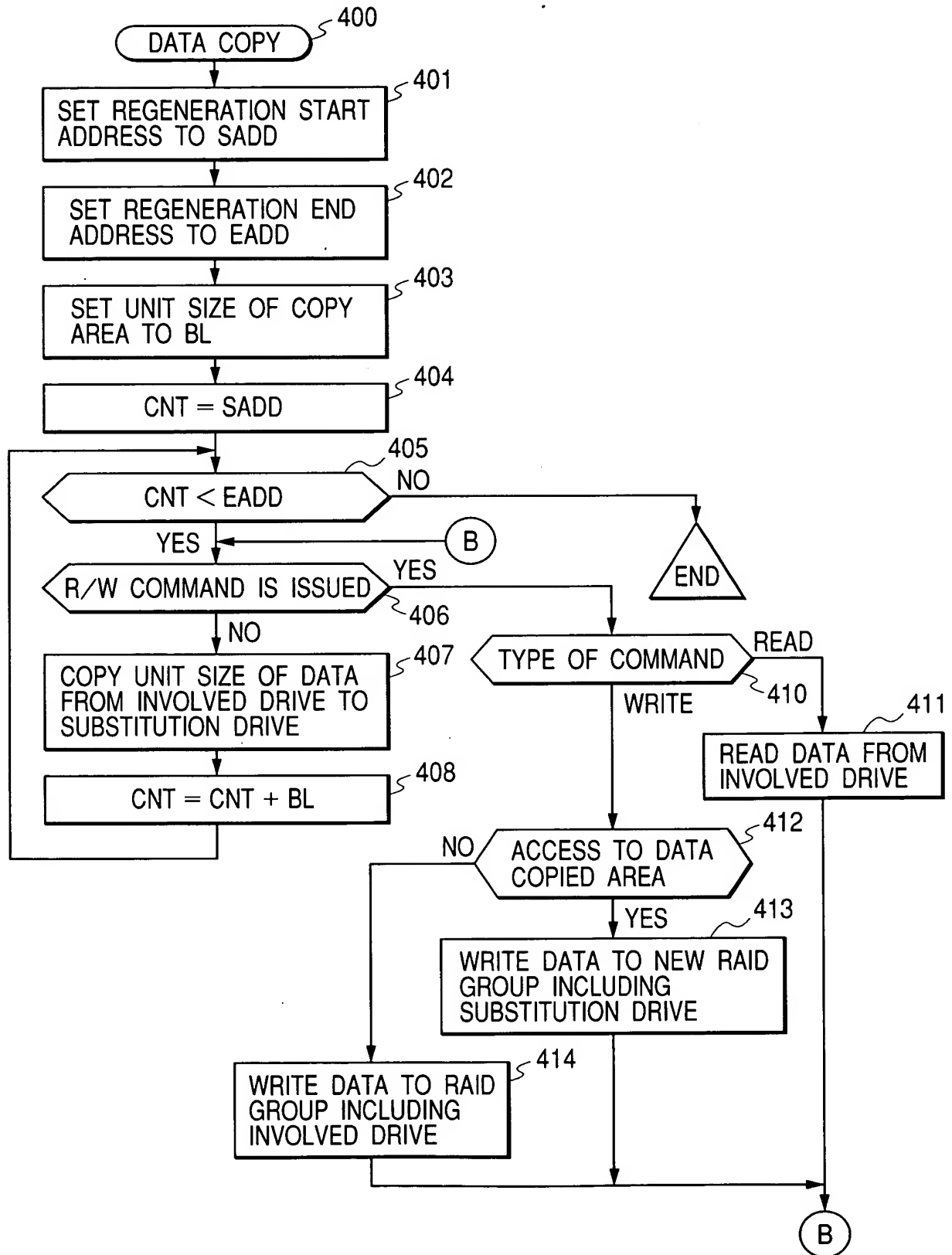


FIG. 18

